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Interrupt priority control within a nested interrupt system

P Kimelman, I Field, RR Grisenthwaite - US Patent App. 10/775,334, 2004 - Google Patents

 ... illustrates a control table used within a **nested** vector **interrupt** controller; [0030 ...

 a stack memory containing multiple sets of **state data**; [0031] FIG. ...

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Interrupt processing control

P Kimelman, I Field - US Patent App. 10/775,335, 2004 - Google Patents

 ... illustrates a control table used within a **nested** vector **interrupt** controller; [0034 ...

 a stack memory containing multiple sets of **state data**; [0035] FIG. ...

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Method for generating interrupt commands in a microprocessor system and relative priority interrupt

...

S Pezzini - US Patent App. 10/717,177, 2003 - Google Patents

 ... REG and PRIORITY STACK used for managing the **nested interrupts**. ... When the processing of any **interrupt** is completed, its ... STACK PUSH/POP of the **state** machine IRQ ...

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Enhanced thread processing

LM Browning - US Patent 6,654,781, 2003 - Google Patents

 ... complicated system calls, which may have **nested** subroutine calls ... such as its thread ID or execution **state**. ... any thread or even by an **interrupt** handler, provided ...

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[PDF] ► Application of patterns to real-time object-oriented software design

RA McKegney - 2001 - uml.org.cn

 ... 3.4: A Simple **State** Diagram.....22 ... on modeling the system **data**. ...

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SYSTEM OF REUSABLE SOFTWARE PARTS FOR IMPLEMENTING CONCURRENCY AND HARDWARE ACCESS, AND METHODS OF ...

VI Miloushev, PA Nickolov - EP Patent 1,224,544, 2002 - freepatentsonline.com

 ... comprises: a connection between said **interrupt** source part ... can obtain variable sets of **data** values from ... in accordance with previously saved persistent **state**. ...

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 Key authors: [P Kimelman](#) - [I Field](#)

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